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Qucs again. Tool
suite Qucs consists
of several
standalone
programs

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interacting with each other through the GUI. There are the GUI itself, The GUI is used to create schematics, setup simulations, display simulation results, writing VHDL code, etc. the backend analogue simulator, The analogue simulator

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is a command line program which is run by ...

Qucs - A Tutorial
release of Qucs 0.0.8 there has been considerable activity centred around finding and correcting a number of bugs in the Qucs digital simulation code.

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Many of these xes are now included in the latest CVS code and will eventually form part of the next Qucs release. This tutorial note is an attempt on my part to communicate

Qucs - A Tutorial
Qucs comes with a document which

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lists the details of its models, and, being open source, there is always the code itself. Most of us end up taking a great deal on trust, and matching curves to data

Qucs - A Tutorial
The aim of this tutorial note is to outline the

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background to these important package extensions and to provide real help to Qucs users who are interested in writing and experimenting with their own models. The text includes a number of illustrative examples for

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readers to try and experiment with. Qucs electronic device and circuit modelling

Qucs - A Tutorial
The purpose of this tutorial note is to introduce readers to a number of techniques that allow SPICE netlists to be simulated by

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Qucs, secondly to indicate the limitations of the current SPICE to Qucs netlist conversion process, and finally to present a preview of how Qucs is likely develop in the future in the area of SPICE netlist compatibility.

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Qucs - A Tutorial
Qucs a truly
universal
simulator. Qucs
0.0.8 was the rst
release to include
digi-tal simulation.
Qucs digital
simulation centres
around VHDL using
the FreeHDL VHDL
compiler to
generate a

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machine code
simulation of a
circuit under test.
Re-lease 0.0.8
includes built-in
models for the
basic digital gates
and a number of
the common
sequential ip-ops.

Qucs - A Tutorial
tutorial
concentrates on

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models that can be simulated using Qucs release 0.0.9. The Qucs built-in operational amplifier model Qucs includes a model for an ideal operational amplifier. It's symbol can be found in the nonlinear components list. This model

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represents an
operational ampli
er

Qucs - A Tutorial
there is Qucs.
Things get easier.
Just select Tools
!Line Calculation in
the menubar or
press Ctrl+3 to
start the
transmission line
calculator. Then

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choose Coupled
Microstrip in the
Transmission Line
Type selection box.
Something likely
shown in
figure 2 should
appear. 3

Qucs - A Tutorial
This manual
describes the
measurement
expressions

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available in "Qucs",
the "Quite
Universal Circuit
Simulator".

Measurement
expressions come
into play whenever
the results of a
"Qucs"simula-tion
run need post
processing.

Examples would be
the conversion of a
simulated voltage

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waveform from
volts to dBV, the
root mean square
value of that
waveform

Qucs - Reference
Manual

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Author: Thierry

Scordilis Subject:

Biasing a BJT

Transistor Created

Date: 3/15/2014

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documentation

This chapter will

describe an RF

design issue using

QUCS. The author

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assume that the basic manipulation of qucs is known. You will find herein mainly a Mac-OSX description that is close to a linux or unices architecture. choice of transistor The choice has been made to choose among the Philips RF

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wideband
transistor library.

Qucs - A Tutorial
this Qucs note are
designed to give
good performance
from low
frequencies to RF
frequencies not
greater than a few
GHz. RF Resistor
Models The
schematic symbol,

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I/V equation and parameters of the Qucs linear resistor model are shown in Figure1. In contrast to this model Figure2 illustrates the ... Qucs - A Tutorial ...

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Qucs Tutorial:
Simulating a
common emitter

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unmute. If
playback doesn't
begin shortly, try
restarting your
device. Up...

Qucs Tutorial:
Simulating a
common emitter
bjt amplifier ...

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Nested Simulations. Qucs allows for nested simulations; as an example we consider an AC analysis together with a parameter sweep. The AC analysis is set up as before, but in addition the value of the capacitor C1 is increased in 5

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steps from 10nF to 100nF. The netlist for this simulation is as follows.

```
Vac:V1 in gnd U="1  
V" f="1 kHz" R:R1  
out in R="1 kOhm"
```

Qucs - A Tutorial
Qucs project: Quite
Universal Circuit
Simulator

Qucs project: Quite
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Universal Circuit Simulator
QUCS or Quite Universal Circuit Simulator is a easy to use software tool to design and simulate electronic circuits. This lesson helps you to become familiar...

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A Tutorial Qucs
Project Quite Qucs,
briefly for Quite
Universal Circuit
Simulator, is a
circuit simulator
with graphical user
interface (GUI). The
GUI is based on
Qt® by Digia ®.
The software aims
to support all kinds
of circuit simulation
types, e.g. DC, AC,

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S-parameter,
Harmonic Balance
analysis, noise
analysis, etc.

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Project Quite
Universal Circuit
Simulator

All users of Qucs
are invited to
contribute to these
examples. If you
want to share a

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schematic or circuit
model do not
hesitate to do so.

Simulation

Examples ... ,

limiters, phase
shifters) and

subsystems (T/R
modules and

reflect arrays) for a
tutorial (tested

with version

0.0.15) given at the
2010 IEEE Radar

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Van Caekenberghe

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